

R E M A R K S

Careful review and examination of the subject application are noted and appreciated.

STATEMENT OF COMMON OWNERSHIP

The present application and U.S. Patent 7,302,160, issued to Wells, were commonly owned by LSI Logic Corporation, Milpitas, California, at the time that the invention of the present application was made.

CLAIM REJECTIONS UNDER 35 U.S.C. §103

The rejection of claims 1-6, 9-13, 15, 16, 18 and 19 under 35 U.S.C. §103(a) as being unpatentable over Wells '160 in view of Arora, US Pub. No. 2004/0114049, and Linzer '102 is respectfully traversed and should be withdrawn.

The rejection of claim 7 under 35 U.S.C. §103(a) as being unpatentable over Wells in view of Arora, Linzer and McGee et al., US Pub. No. 2003/0117530, is respectfully traversed and should be withdrawn.

The rejection of claims 20-24 under 35 U.S.C. §103(a) as being unpatentable over Wells in view of Hua et al., US Pub. No. 2004/0161154, is respectfully traversed and should be withdrawn.

The Wells reference is not valid prior art under 35 U.S.C. §103(c) as amended by the American Inventors Protection Act

of 1999. "Subject matter developed by another person which qualifies as prior art only under one or more of subsections (e), (f), and (g) of section 102 of this title, shall not preclude patentability under this section where the subject matter and the claimed invention were, at the time the invention was made, owned by the same person or subject to an obligation of assignment to the same person." (35 U.S.C. §103(c).) The Wells reference was owned by LSI Logic Corporation at the time of invention of the present application. The present application was also owned by LSI Logic Corporation (now LSI Corporation) at the time of invention. Therefore, the Wells reference is disqualified as prior art for a 35 U.S.C. §103(a) rejection via §102(e) and the rejections should be withdrawn.

In particular, the present application was filed on November 14, 2003 and was assigned to LSI Logic Corporation. The assignment was recorded in the United States Patent and Trademark Office at reel/frame 014707/0189. A copy of the PAIR patent assignment abstract for the present application is attached to this response as Exhibit A. The filing of the application was after November 29, 1999 (the effective date for the change to 35 U.S.C. §103(c)). Therefore, the amendment to 35 U.S.C. §103(c) by the American Inventors Protection Act of 1999 applies.

Applicant's representative respectfully traverses the Official Notice on page 6 of the Office Action. ROM cannot be

written to and thus cannot act as a buffer. RAM with sufficient capacity to hold entire frames of video is not commonly integrated into processors as the two types of circuits usually employ different fabrication techniques that are optimized to the respective circuit operations. As such, the Office is respectfully requested to either (i) provide evidence showing that including a buffer in a processor is well known in the art where the buffer has a capacity sufficiently large to handle frames of video or (ii) withdraw the notice.

Regarding the obvious arguments on pages 8 and 13 of the Office Action, Applicant's representative stated that given the teachings of the application, one of ordinary skill in the art would understand the claimed invention. No admission was made that any part of the invention was obvious. As such, the Office is respectfully requested to withdraw the incorrect obviousness assertions.

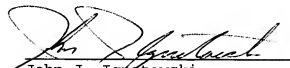
Accordingly, the present application is in condition for allowance. Early and favorable action by the Examiner is respectfully solicited.

The Examiner is respectfully invited to call the Applicant's representative between the hours of 9 a.m. and 5 p.m. ET at 586-498-0670 should it be deemed beneficial to further advance prosecution of the application.

If any additional fees are due, please charge Deposit
Account No. 12-2252.

Respectfully submitted,

CHRISTOPHER P. MAIORANA, P.C.



John J. Ignatowski
Registration No. 36,555

Dated: September 23, 2008

c/o Lloyd Sadler
LSI Corporation

Docket No.: 1496.00351 / 03-1918



Patent eBusiness

- Electronic Filing
- Patent Application Information (PAIL)
- FEES
- Supplemental Resources & Support

Patent Information

- Patent Guidance and General Info
- Controlled Language
- Patent Office Procedures
- Resources & Publications

Patent Searches

- Patent Official Grants
- Search Panels & Applications
- Search Biological Sequences
- Control, Products & Services

Other

- Candidates
- Trademarks
- Policy & Law
- Patents

Secure Patent Application Information Retrieval

XSL

Download

10713,441

Commercial detector with a start of active video detector

Select Application Information (Image File) Publication Address & Assignments Display Application New Case Data History Wrapper Document Attorney/Agent Reference Review

Assignments Data

Patent Assignment Abstract of Title

Total Assignments: 2

Application #: 10713441

PCT #: NONE

Inventor: Elliot N. Linzer

Title: Commercial detector with a start of active video detector

Assignment: 1

Reel/Frame: 014707 / 0189

Received: 11/21/2003

Conveyance: ASSIGNMENT OF ASSIGNORS INTEREST (SEE DOCUMENT FOR DETAILS).

Assignor: LINZER, ELLIOT N.

Assignee: LSI LOGIC CORPORATION

1621 BARBER LANE

MS: D-106 LEGAL

MILPITAS, CALIFORNIA 95035

Correspondent: LSI LOGIC CORPORATION

CHRISTOPHER P. MAJORANA

1621 BARBER LANE

MS: D-106 LEGAL

MILPITAS, CA 95035

Assignment: 2

Reel/Frame: 020548 / 0977

Received: 02/19/2008

Conveyance: MERGER (SEE DOCUMENT FOR DETAILS).

Assignor: LSI SUBSIDIARY CORP.

Assignee: LSI CORPORATION

1621 BARBER LANE

MILPITAS, CALIFORNIA 95035

Correspondent: PETER P. SCOTT

4420 ARROWSWEST DR

MS L-203

COLORADO SPRINGS, CO 80907

Order Certified Application As Filed Order Certified File Wrapper View Order List

Issue Dt: 05/19/2005
 Publication #: US20050108745
 Pub Dt: 05/19/2005

Pages: 2

Mailed: 06/09/2004

Recorded: 11/14/2003

Exec Dt: 11/13/2003

Pages: 20

Mailed: 02/25/2008

Recorded: 02/19/2008

Exec Dt: 04/04/2007

Exhibit A
 10/7/13, 44/